

*other
S1 and PCB*

FIG 1

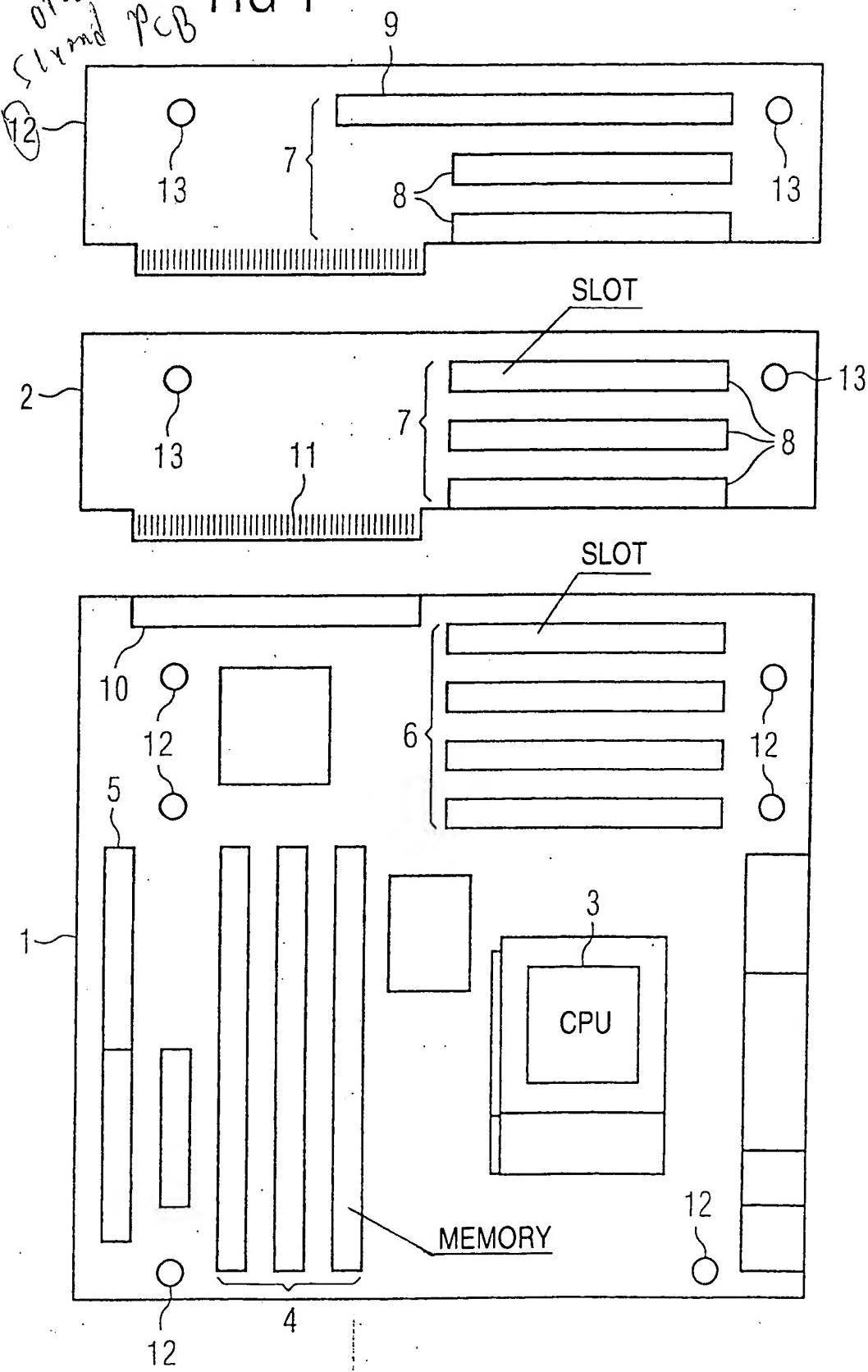


FIG 2

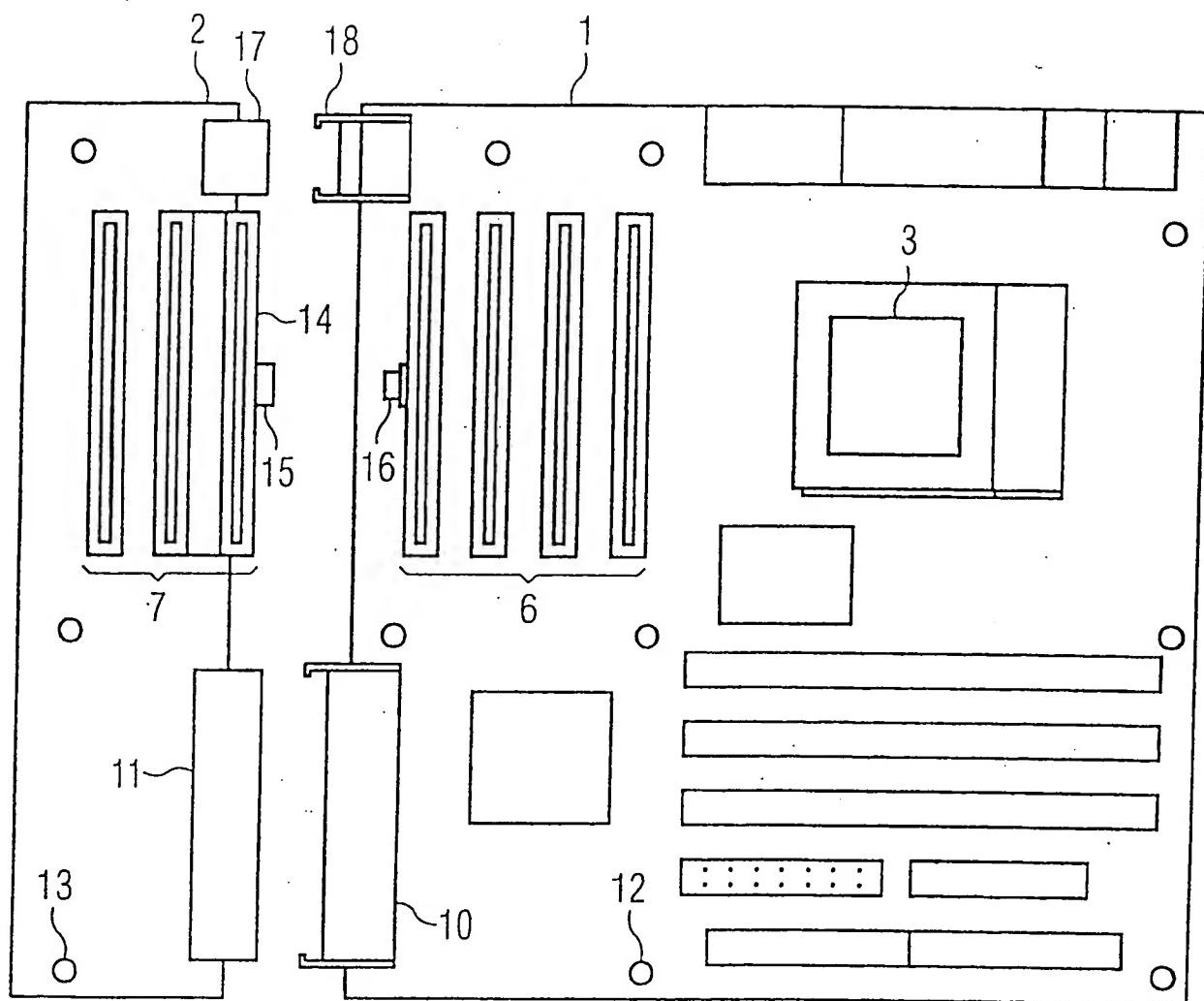


FIG 3

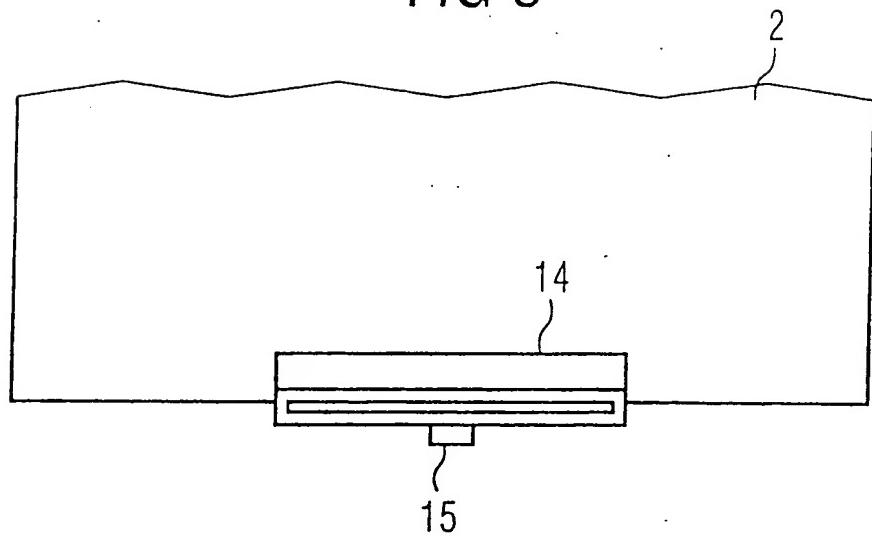


FIG 4

